



## **NXP and FatPipe Networks Collaborate to Deliver SD-WAN Solution**

May 28, 2019

*Industry-leading SD-WAN solution now available on Layerscape multi-core Arm®-based processors*

**Taipei, May 28, 2019 (COMPUTEX 2019)** – NXP Semiconductors N.V. (NASDAQ: NXPI) and FatPipe Networks, the inventor and multiple patents holder of software-defined wide area networking (SD-WAN) technology, today announced that FatPipe's SD-WAN client for branch routers has been ported to NXP's Layerscape family of 64-bit network processors. This means that operators and enterprise customers now have a broader range of price, power and performance points to choose from when selecting branch router hardware for their SD-WAN requirements.

SD-WAN is a specific application of software-defined networking (SDN) technology applied to WAN connections such as broadband internet, 4G, LTE, or MPLS. It connects enterprise networks — including branch offices and data centers — over large geographic distances. SD-WAN enables enterprises to reduce their dependence on proprietary hardware and balance their use of less expensive internet connectivity with more expensive, fixed access circuits such as MPLS.

“This announcement underscores the growing ecosystem of enterprise customer premise solutions supporting our Arm-based Layerscape multi-core processor family,” commented Noy Kucuk, Vice President, Digital Networking at NXP. “We are excited to be working with FatPipe to deliver an expanded value proposition to customers.”

“We are excited to partner with NXP to offer the first SD-WAN solution available on an Arm-based processor. This demonstrates the flexibility and processor independence of FatPipe technology,” added Dr. Ragula Bhaskar, CEO, FatPipe Networks. “Our cooperation with NXP will enable a greater choice of solutions for our customers addressing the multi-function uCPE solutions for providers, enterprises and retail. The integrated platform offers improved performance for intelligent edge at remote offices and branch offices.”

NXP's Layerscape series processors built on Arm® core technology extends performance to the smallest form factor— from power-constrained single-core networking all the way up to high-performance 16-core devices supporting 100Gbps throughput. NXP will demonstrate a variety of Layerscape platforms to invited customers during the Computex event in Taipei, 28th May – 1st June.

### **About FatPipe**

FatPipe Networks invented the concept of software-defined wide area networking (SD-WAN) and hybrid WANs that eliminate the need for hardware and software, or cooperation from ISPs and allows companies and service providers to control multi-link network traffic. FatPipe currently has 11 U.S. patents and more than 180 technology claims related to multipath, software-defined networking and selective encryption of broadband networks. FatPipe technology provides the world's best intra-corporate wide area network solutions that transcend Internet and other network failures to maintain business continuity and high transmission security. FatPipe, with several thousand customers, has offices in the United States and around the world, and more than 700 resellers worldwide including almost all national resellers in the US. For more information, visit [www.fatpipe.com](http://www.fatpipe.com).

### **About NXP Semiconductors**

NXP Semiconductors N.V. (NASDAQ:NXPI) enables secure connections and infrastructure for a smarter world, advancing solutions that make lives easier, better and safer. As the world leader in secure connectivity solutions for embedded applications, NXP is driving innovation in the secure connected vehicle, end-to-end security & privacy and smart connected solutions markets. Built on more than 60 years of combined experience and expertise, the company has over 30,000 employees in more than 30 countries and posted revenue of \$9.26 billion in 2017. Find out more at [www.nxp.com](http://www.nxp.com)

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