



NXP Debuts New MCX Portfolio of Microcontrollers for the Next Era of Advanced Industrial and IoT Edge Computing

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- *The new MCX microcontroller (MCU) portfolio features four new series of devices, built on a common platform and supported by NXP's widely adopted MCUXpresso suite of development tools and software to simplify product development*
- *The Arm® Cortex®-M core-based MCX portfolio includes the high-performance MCX N series, cost-optimized and analog-focused MCX A series, low-power wireless connectivity MCX W series and ultra-low power MCX L series*
- *The new portfolio features the first instantiation of a specialized, NXP-designed machine learning accelerator to enable high performance inference at the edge*

EINDHOVEN, The Netherlands, June 14, 2022 (GLOBE NEWSWIRE) -- NXP Semiconductors (NASDAQ: NXPI) today announced the new MCX portfolio of microcontrollers, designed to advance innovation in smart homes, smart factories, smart cities and across many emerging industrial and IoT edge applications. The portfolio includes four series of devices built on a common platform and is supported by the widely adopted MCUXpresso suite of development tools and software. This combined offering allows developers to maximize software reuse across the portfolio to speed development. The portfolio also features the first instantiation of NXP's new, specialized neural processing unit (NPU) for accelerating inference at the edge, delivering up to 30x faster machine learning throughput compared to a CPU core alone.

Driven by the proliferation of edge devices, the MCU has evolved dramatically over the last few decades. While they are at the heart of many of today's edge applications, the next generation of intelligent applications will require a new class of MCUs that allow developers to navigate a complex landscape of power, performance and security requirements, as well as connectivity options, while balancing total system cost and energy efficiency. Built on a common foundation of core technologies and supported by a unified software suite for maximal software reuse, the MCX portfolio will enable the flexibility needed to address this challenge. The breadth of the MCX portfolio allows developers to select devices that best fit their application needs, freeing them to invest in differentiating aspects of their design.

"As we approach the milestone of 75 billion connected devices, we are entering a new era of edge computing, requiring us to fundamentally rethink how to best architect a flexible MCU portfolio that is scalable, optimized and can be the foundation for energy efficient industrial and IoT edge applications today and in the decades to come," said Ron Martino, Executive Vice President and General Manager of Edge Processing for NXP Semiconductors. "By building on our strong legacy in MCUs, this new portfolio will offer the performance and integration needed to address the real-time workloads for the next wave of innovation."

The four series in the MCX portfolio are designed for ease of use, to simplify migration and to scale up or down as needed with maximal software reuse to minimize development cost. The portfolio is based on high-performance Arm Cortex-M cores and integrates a comprehensive set of peripherals for design flexibility. MCX devices feature up to 4 MB of on-chip flash memory, low power cache and advanced memory management controllers, plus up to 1MB of on-chip SRAM to further enhance real-time performance of edge applications.

Machine learning and run-time inference will be supported by NXP's eIQ® ML software development environment. Developers can utilize the easy-to-use tools offered by eIQ to train ML models targeting either the NPU or the CPU core and deploy them on the MCU. MCX families that are built following NXP's security-by-design approach will offer secure boot with an immutable root-of-trust, hardware accelerated cryptography and, on select families, a built-in EdgeLock® secure subsystem.

About the MCX Portfolio:

- The MCX N Advanced series is designed for secure, intelligent applications, with families that include an integrated EdgeLock secure subsystem and dedicated NPU with a high efficiency compute architecture for real-time inference.
- The MCX A Essential series is optimized to provide critical functionality for a broad range of applications such as motor control where cost constraints, advanced analog capabilities such as high-precision data converters and fast time to market are key considerations.
- The MCX W Wireless series offers low-power narrowband connectivity, including Bluetooth Low Energy. Designed to simplify adding wireless connectivity to IoT devices, its energy-efficient radio helps extend the battery life of small connected systems.
- The MCX L Ultra-Low Power series is designed for power-critical applications. With one of the industry's lowest static and dynamic power consumption, these devices will help extend battery life significantly compared to traditional MCUs.

For more information on the series, please visit nxp.com/MCX or contact NXP Sales worldwide.

Visit NXP's booth at Embedded World 2022 (Booth 4A-222) to see a demonstration of the MCX W series, part of the MCX portfolio.

About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) enables a smarter, safer and more sustainable world through innovation. As a world leader in secure connectivity solutions for embedded applications, NXP is pushing boundaries in the automotive, industrial & IoT, mobile, and communication infrastructure markets. Built on more than 60 years of combined experience and expertise, the company has approximately 31,000 employees in more than 30 countries and posted revenue of \$11.06 billion in 2021. Find out more at www.nxp.com.

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A photo accompanying this announcement is available at <https://www.globenewswire.com/NewsRoom/AttachmentNg/08aa8b75-da22-45eb-bab7-a328b3b7e74a>



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Source: NXP USA, Inc.