



NXP Introduces Industry's Lowest Power Voltage Translators

February 23, 2016

Enables engineers with flexibility to design high performance applications with freedom

Embedded World 2016 – Nuremberg, Germany, February 23, 2016 – NXP Semiconductors N.V. (NASDAQ: NXPI) today introduced the AXP family of logic translators, designed for low-power and high-performance applications. The family of AXP translators is the lowest power voltage translator solution in the market while also offering the widest range of voltage translation from 0.7 volts to 5.5 volts. The AXP translators (AXPnT) are ideal for high performance applications with Microprocessors/Microcontrollers/SOCs operating at ultra-low voltages below 1.8 volts that still need to interface with I/O peripheral solutions typically operating at 2.5 V, 3.3 V, or even 5.5 V.

In 2014, NXP introduced the AXP family of logic devices, offering the lowest power logic functions in the industry. With today's introduction of the AXPnT, NXP now offers solutions to interfacing Microprocessors/Microcontrollers/ASICs operating as low as 0.7 V to peripheral devices operating as high as 5.5 V.

The AXPnT includes standard buffers, as well as gates and configurable functions. They are available in both leaded and leadless package options, including NXP's 5- and 6-pin GX packages. These packages are the smallest leadless logic packages in the industry. By providing the lowest power voltage translator products, NXP Semiconductors continues to support the migration of applications to lower voltages. The result is greater power savings for battery powered applications, while at the same time facilitating the interfacing of lower-voltage ASICs, microprocessors, microcontrollers, or SOC chips with legacy higher-voltage peripheral devices in the market place.

Features of AXPnT devices

- Dual supply voltage level translation
 - V_{CCI} = 0.7 V to 2.75 V
 - V_{CCO} = 1.2 V to 5.5 V
- Very low dynamic power dissipation (C_{PD})
- Schmitt-trigger action on all inputs
- 12mA balanced output drive
- Over-voltage tolerant inputs and outputs
- Leaded & Leadless Package Options
- Fully specified (-40 to +85 °C)
- Pb-free, RoHS compliant and Dark Green

Availability

The AXP translator family of devices are expected to sample in February 2016.

Links

Product information: www.nxp.com/logic

50 Years of Logic: Leading volume supplier of Logic worldwide

NXP Semiconductors is committed to the logic market and continually invests in new process and package technologies, as well as packaging facilities, to offer a leading-edge portfolio. Over the last 50 years, the NXP logic business—starting as Philips Semiconductors—has supported growing global demand for logic. As the No. 1 volume supplier in the world, NXP can support the highest volume logic requirements, while offering a broad variety of industry-leading package solutions.

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About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ: NXPI) enables secure connections and infrastructure for a smarter world, advancing solutions that make lives easier, better and safer. As the world leader in secure connectivity solutions for embedded applications, NXP is driving innovation in the secure connected vehicle, end-to-end security & privacy and smart connected solutions markets. Built on more than 60 years of combined experience and expertise, the company has 45,000 employees in more than 35 countries.

Forward-looking Statements

This document includes forward-looking statements which include statements regarding NXP's business strategy, financial condition, results of operations and market data, as well as other statements that are not historical facts. By their nature, forward-looking statements are subject to numerous factors, risks and uncertainties that could cause actual outcomes and results to be materially different from those projected. Readers are cautioned not to place undue reliance on these forward-looking statements. Except for any ongoing obligation to disclose material information as required by the United States federal securities laws, NXP does not have any intention or obligation to publicly update or revise any forward-looking statements after NXP distributes this document, whether to reflect any future events or circumstances or otherwise. For a discussion of potential risks and uncertainties, please refer to the risk factors listed in NXP's SEC filings. Copies of NXP's SEC filings are available from the SEC website, www.sec.gov.

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