NXP® Unveils Highest Performance Layerscape® Networking and Data Center Offload System-on-Chip Solution

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Sixteen high-performance Arm® 64-bit cores, advanced acceleration and 100 Gbit/s Ethernet in low-power FinFET process technology

SAN JOSE, Calif., Oct. 04, 2017 (GLOBE NEWSWIRE) -- **LINLEY PROCESSOR CONFERENCE** -- NXP Semiconductors N.V. (NASDAQ:NXPI), a worldwide leader in advanced secure connectivity solutions, today announced the highest performance member of the Layerscape family, the LX2160A SoC. The LX2160A is specifically designed to enable challenging high-performance network applications, network edge compute, and data center offloads. Trusted and secure execution of virtualized cloud workloads at the edge is driving new distributed computing paradigms.

The LX2160A features sixteen high-performance Arm Cortex®-A72 cores running at over 2 GHz in a sub 30-watt power envelope, supporting both the 100 Gbit/s Ethernet and PCIe Gen4 interconnect standards. In addition, it provides L2 switching at wire rate and includes acceleration for data compression and 50 Gbit/s IPSec cryptography. The NXP Layerscape processor family, stretching from single-core sub-1W processors to the 16-core LX2160A, provides the broadest family of 64-bit Arm processors available[1].

“Edge processing will drive the next phase of networking, computing and IoT infrastructure growth,” said Tareq Bustami, senior vice president and general manager at NXP Semiconductors. “As workloads move from the cloud to the edge they benefit from reduced latencies, greater security and improved resiliency. The performance and the bandwidth integrated into the LX2160A provides an ideal platform for a variety of networking, data analytics and data processing workloads.”

“Network service providers, cloud companies, and industrial IoT companies are developing edge computing, drawn to the technology’s ability to deliver cloud-computing services at lower latency and with greater privacy, while consuming less WAN bandwidth,” said Bob Wheeler, principal analyst with The Linley Group. “Integrating 16 cores and 100 Gbit/s Ethernet, NXP’s Layerscape LX2160A processor helps the Arm ecosystem meet the requirement of high-performance edge-computing applications.”

NXP supports and drives the rich Arm ecosystem for virtualization, building on the foundations of open source projects for cloud and network function virtualization including Open Daylight, OpenStack, and OP-NFV®. NXP Arm processors incorporate hardware for virtualization technologies such as KVM and Linux® containers and hardware acceleration of network virtualization. NXP also supports industry-standard APIs for virtualization, including DPDK, OVS, and Virtio, and standard enterprise Linux distributions, such as Debian and Ubuntu.

Key elements of the Layerscape LX2160A SoC can be found at www.nxp.com/LX2160. Silicon samples and a reference board will be available in Q1 2018.

[1] *A Guide to Multicore Processors, Fourth Edition*; 2017. The Linley Group

About NXP Semiconductors

NXP Semiconductors N.V. (NASDAQ:NXPI) enables secure connections and infrastructure for a smarter world, advancing solutions that make lives easier, better and safer. As the world leader in secure connectivity solutions for embedded applications, NXP is driving innovation in the secure connected vehicle, end-to-end security & privacy and smart connected solutions markets. Built on more than 60 years of combined experience and expertise, the company has 31,000 employees in more than 33 countries and posted revenue of $9.5 billion in 2016. Find out more at www.nxp.com.

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